Indzawo Optic Connect

AI Server Switch Chip

Overview

AI Switch Baseboard Printed Circuit Board Fabrication is a core aspect of modern AI computing infrastructure. The AI switch baseboard—also known as AI interconnect or switched baseboard—is specifically designed for artificial intelligence servers and high-performance computing (HPC). The chip shortage is spreading to power and management controller silicon, threatening server shipments as vendors prioritize capacity for higher-margin AI server products. Market watcher TrendForce has downgraded its server shipment grwoth forecast for the whole of 2026 from 20 percent to 13. Cyntec, a leading provider of power and passive components, is responding to the growing demand for high power density and thermal efficiency in AI servers. To support this trend, especially in High Voltage Direct Current (HVDC) power architectures data centers, Cyntec has developed a series of. With advancements in artificial intelligence (AI) and machine learning, enterprise servers have become extremely power-hungry as they simultaneously process a large amount of data and storage. With features like high performance, high bandwidth, low latency, strong scalability, and high reliability, the AI switch baseboard PCB has become an irreplaceable core.

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Cyntec has developed compact, high-efficiency, low-loss power inductors specifically designed for applications such as AI server SXM accelerator cards, UBB motherboards, and Switch.

Designed for both cloud data center and AI fabrics, Teralynx devices are programmatically configurable, enabling a single device to perform optimally in data center top-of-rack (ToR), spine/leaf, and edge ...

With features like high performance, high bandwidth, low latency, strong scalability, and high reliability, the AI switch baseboard PCB has become an irreplaceable core component in modern AI clusters ...

Explore how innovations in power devices, gate drivers, and DSP-based controllers tackle AI servers'' high energy demands, optimizing efficiency in data centers.

AI server high-speed transmission leader Astera Labs delivered strong performance in 2025''s first quarter, announcing record results this week alongside a media event in Taiwan outlining ...

Systems AI now gobbling up power and management chips for servers Bad news for multiple general server components as vendors switch to more lucrative gear

Learn how AI workloads are reshaping server architecture with accelerators, CXL memory pooling, high-speed interconnects, and advanced cooling.

The proposed solution in this article eliminates the limitations of legacy hot-swap controllers and enables the design of a reliable input protection solution for a 48V AI server.

Astera Labs'' P-Series Scorpio Smart Fabric Switches are purpose-built to overcome these challenges by enabling seamless mixed traffic head-node connectivity across diverse PCIe generations and ...

CPU requirements for AI workloads are multiplying, driving intensifying shortages and price hikes — Intel already shifting production from consumer chips to Xeon as inference workloads ...

Contact Us

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