

How to connect the optical module to the PHY



Overview

In this article, I'll run over the important guidelines for working with an optical PHY that would be found in a modern network switch, the layout topology, and how to deal with power in these components.



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100G to 1.6T Optical Module PHY Product Selection Guide Broadcom's Optical Module PHY portfolio spans multiple technology nodes — 16nm, 7nm and now 5nm, with data rates from 100 Gbs to 1.6 ...



Use one of the PHY's LED pins, select LINK/ACTIVITY, and connect it to the CATHODE pin of an LED. This can be used to indicate when the fiber is linked and has data activity present.



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Below is the PHY-PHY RMI connection in this mode. The configuration above is recommended for RMI connections between PHYs. The configuration also works ...



Learn how the Serial Framing Interface (SFI) enables high-speed data transmission in 10G SFP+ and 40G QSFP+ optical modules. Explore SFI channels, XGMII conversion, and module ...



Via the serial data ports RXP TXP, it can directly connect QSFP optical or CR-type transceivers. Ethernet data are passed to/from FPGA fabric via the standard MII interface.



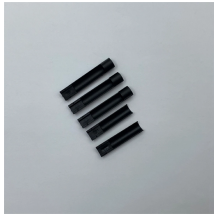
Analyzing Broadcom's Sian3 and Sian2M 200G/lane DSP technologies. Sian3 (3nm/SMF) and Sian2M (5nm/MMF) support 800G and 1.6T optical modules, meeting the high ...



PHY is the abbreviation for physical layer. It is used to connect a device to the physical medium e.g., the USB controller has a PHY to provide functions such as serialization, de-serialization, encoding, ...



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Our customer wants to use SFP modules (1000 Base-SX standard) to send some data to a remote computer. As there is only very little data to be transferred (actually no real need for ...



We have a Colibri module (imx6ULL) mounted in a custom carrier board and we want to use the 2nd ethernet interface (eth1) as an optical fiber link. ...



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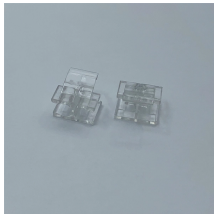
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The optical module layout refers to the physical placement of optical components, including laser sources, photodetectors, driver/TIA circuits, and PHY chips, on the module PCB.



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The checklist described is a list of suggestions that will help a PHY operate the closest to ideal behavior. Following these suggestions can help prevent unwanted issues.

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